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PHONE NO.:****FROM: Aaron Wininger****DIRECT DIAL NO.: +1.650.843.3375****E-MAIL: awininger@ssd.com****RE: U.S. Patent Application****Serial No.: 10/655,696****Entitled: "FABRICATION METHOD FOR HETEROJUNCTION
BIPOLAR TRANSISTOR"****Filed: September 5, 2003****Inventors: Cheng-Wen FAN and Hua-Chou TSENG****Our Ref.: 61994.00002****To whom it may concern:**

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Sincerely,

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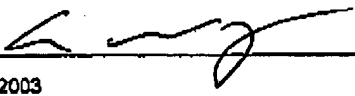
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application Number	10/655,696
		Filing Date	September 5, 2003
		First Named Inventor	Cheng-Wen Fan
		Art Unit	2812
		Examiner Name	Unknown
Total Number of Pages in This Submission	5	Attorney Docket Number	61994.00002

ENCLOSURES (check all that apply)		
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Firm or Individual name	Aaron Wininger, Reg. No. 45,229 Squire, Sanders & Dempsey L.L.P. 600 Hansen Way Palo Alto, CA 94304-1043
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PATENT
Docket No. 61994.00002

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Date: 12/10/03By: 
Aaron Winingar

In Re Application of	
Cheng-Wen FAN and Hua-Chou TSENG	
Serial No. 10/655,696	Examiner: Unknown
Filing Date: September 5, 2003	Group Art Unit: 2812
For: FABRICATION METHOD FOR HETEROJUNCTION BIPOLAR TRANSISTOR	

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 - ☐ Attorney Docket No.
 - ☐ Other -

In re FAN, et al.
U.S. Patent Application No.: 10/655,696

Page 1 of 2
62333

PATENT
Docket No. 61994.00002

in that the filing receipt should read as follows:

Applicant(s)

**Cheng-Wen Fan, Hsin-Chu Hsien, TAIWAN
Hua-Chou Tseng, Hsin-Chu City, TAIWAN**

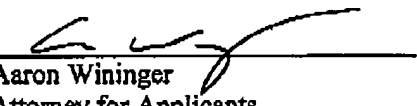
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Date: 12/10/03

Respectfully submitted,

Squire, Sanders & Dempsey L.L.P.
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By: 
Aaron Wininger
Attorney for Applicants
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10/655,696	09/05/2003	2812	768	61994.00002	10	21	3

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SQUIRE, SANDERS & DEMPSEY L.L.P.
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DATES ENTERED

Applicant(s)

Cheng-Wen Fan, Hsin-Chu Hsien, TAIWAN;
Hua-Chou Tseng, ~~Hsin-Chu City~~, TAIWAN;
Hsin-Chu City

DEC 08 2003

Assignment For Published Patent Application

UNITED MICROELECTRONICS CORP., HSIN-CHU CITY, TAIWAN; BY

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ATTORNEY *[Signature]*
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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/03/2003

Projected Publication Date: 03/10/2005

Non-Publication Request: No

Early Publication Request: No

Title

Fabrication method for heterojunction bipolar transistor

Preliminary Class

438

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